



XUP USB-JTAG Programming Cable

Connector(s):

Micro USB

Xilinx 14-pin JTAG connector

- Compatible with Xilinx Platform Cable USB

- Compatible with Full-Speed and 1.5V systems
 Compatible with Full-Speed and Hi-Speed USB ports
 Works with ChipScope, EDK, and System Generator
 Indirectly programs SPI and Parallel FLASH memory
- · Ships with the XUP Fly Wire Assembly

USB programming cable for all Xilinx devices

The XUP-USB programming cable is compatible with Xilinx's Platform Cable USB. It can directly program Xilinx FPGAs CPLDs, and programming ROMs, and indirectly program SPI and parallel Flash ROMs

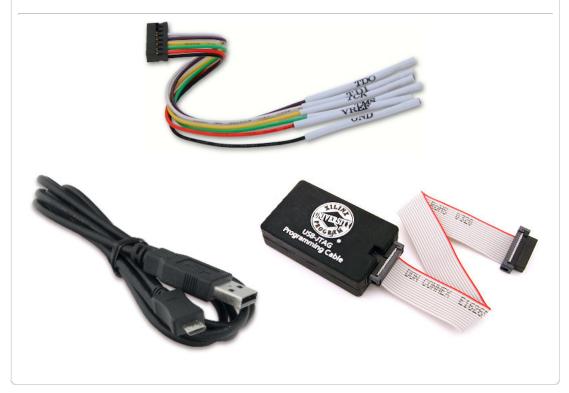
FPGA TECHNOLOGIES PROVIDED BY

with system voltages ranging from 1.5V to 5V. The cable is compatible with full-speed and high-speed USB ports, and is directly accessed from ISE, Chipscope, EDK, and other Xilinx CAD tools.

For more information, please see the Platform Cable USB datasheet, available here.

NOTES:

- · Xilinx iMPACT software is required for programming and
- This cable is not needed for Digilent FPGA boards as our boards are designed with this functionality natively.



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