

RJH65T47DPQ-A0

650V - 45A - IGBT Application: Power Factor Correction circuit R07DS1291EJ0101 Rev.1.01 Oct 22, 2015

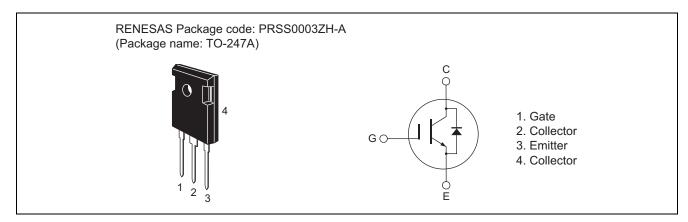
Features

- Low collector to emitter saturation voltage
 V_{CE(sat)} = 1.8 V typ. (at I_C = 45 A, V_{GE} = 15 V, Ta = 25°C)
- Built in fast recovery diode in one package
- Trench gate and thin wafer technology (G7H series)
- High speed switching

 t_f = 45 ns typ. (at V_{CC} = 400 V, V_{GE} = 15 V , I_C = 45 A, Rg = 10 Ω , Ta = 25°C, Inductive load)

- Operation frequency $(20kHz \le f < 100kHz)$
- Not guarantee short circuit withstand time

Outline



Absolute Maximum Ratings

 $(Tc = 25^{\circ}C)$

n	Symbol	Ratings	Unit
	V _{CES} / V _R	650	V
	V _{GES}	±30	V
Tc = 25 °C	lc	90	Α
Tc = 100 °C	lc	45	Α
	Ic(peak) Note1	335	Α
Tc = 25 °C	I _{DF}	30	Α
Tc = 100 °C	I _{DF}	15	А
orward peak current	I _{DF} (peak) Note1	100	А
	P _C Note 2	375	W
pedance (IGBT)	θј-с	0.40	°C/W
sistance (Diode)	θj-cd	1.33	°C/W
	Tj Note2	175	°C
	Tstg	-55 to +150	°C
	Tc = 25 °C Tc = 100 °C Tc = 25 °C Tc = 100 °C Tc = 100 °C rward peak current pedance (IGBT)	Vces / VR Vges Vges Tc = 25 °C Ic Ic(peak) Note1 Ic(peak) Note1 Tc = 25 °C IDF IDF IDF IDF(peak) Note1 Pc Note 2 pedance (IGBT) θj-c sistance (Diode) θj-cd Tj Note2 Tj Note2	VCES / VR 650 VGES ±30 TC = 25 °C Ic 90 TC = 100 °C Ic 45 Ic(peak) Note1 335 TC = 25 °C IbF 30 TC = 100 °C IbF 15 orward peak current IbF(peak) Note1 100 Pc Note 2 375 opedance (IGBT) θj-c 0.40 sistance (Diode) θj-cd 1.33 Tj Note2 175

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Please use this device in the thermal conditions which the junction temperature does not exceed 175°C. Renesas IGBT Application Note is disclosed about reliability test and application condition up to 175°C.

Electrical Characteristics

 $(Ta = 25^{\circ}C)$

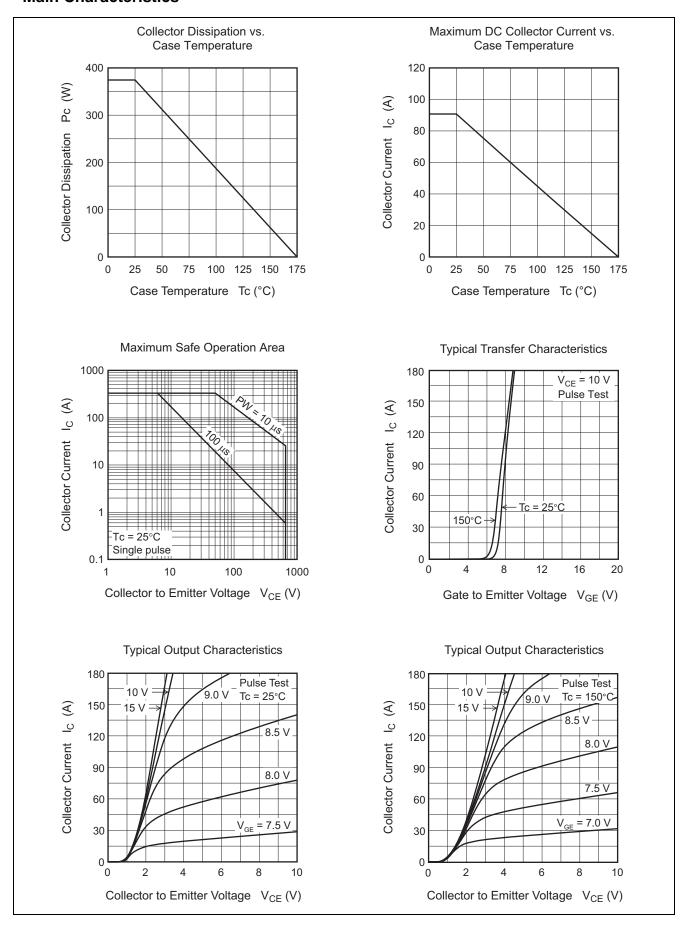
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Zero gate voltage collector current / Diode reverse current	I _{CES} / I _R	_	_	100	μА	V _{CE} = 650 V, V _{GE} = 0
Gate to emitter leak current	Iges	_	_	±1	μΑ	$V_{GE} = \pm 30 \text{ V}, V_{CE} = 0$
Gate to emitter cutoff voltage	V _{GE(off)}	4.0	_	7.0	V	$V_{CE} = 10V, I_{C} = 1.5 \text{ mA}$
Collector to emitter saturation voltage	V _{CE(sat)}		1.8	2.4	V	Ic = 45 A, V _{GE} = 15V Note3
Input capacitance	Cies	_	3000	_	nC	V _{CE} = 25 V
Output capacitance	Coes	_	100	_	nC	V _{GE} = 0
Reveres transfer capacitance	Cres	_	60	_	nC	f = 1 MHz
Total gate charge	Qg	_	127	_	nC	VGE = 15 V
Gate to emitter charge	Qge	_	23	_	nC	VCE = 400 V
Gate to collector charge	Qgc	_	57	_	nC	IC = 45 A
Turn-on delay time	t _{d(on)}	_	45	_	ns	$V_{CC} = 400 \text{ V}$ $V_{GE} = 15 \text{ V}$ $I_{C} = 45 \text{ A}$ $Rg = 10 \Omega$ $T_{C} = 25 \text{ °C}$ Inductive load Note4
Rise time	tr	_	33	_	ns	
Turn-off delay time	t _{d(off)}	_	190	_	ns	
Fall time	t _f	_	45	_	ns	
Turn-on loss energy	Eon	_	0.52	_	mJ	
Turn-off loss energy	E _{off}	_	0.56	_	mJ	
Total switching energy	E _{total}	_	1.08	_	mJ	
Turn-on delay time	t _{d(on)}	_	35	_	ns	$Vcc = 400 \text{ V}$ $VcE = 15 \text{ V}$ $Ic = 45 \text{ A}$ $Rg = 10 \Omega$ $Tc = 150 \text{ °C}$ Inductive load Note4
Rise time	tr	_	33	_	ns	
Turn-off delay time	t _{d(off)}	_	186	_	ns	
Fall time	t _f	_	55	_	ns	
Turn-on loss energy	Eon	_	0.69	_	mJ	
Turn-off loss energy	E _{off}	_	0.77	_	mJ	
Total switching energy	E _{total}	_	1.46	_	mJ	
FRD forward voltage	VF		1.7	2.2	V	I _F = 15 A Note3

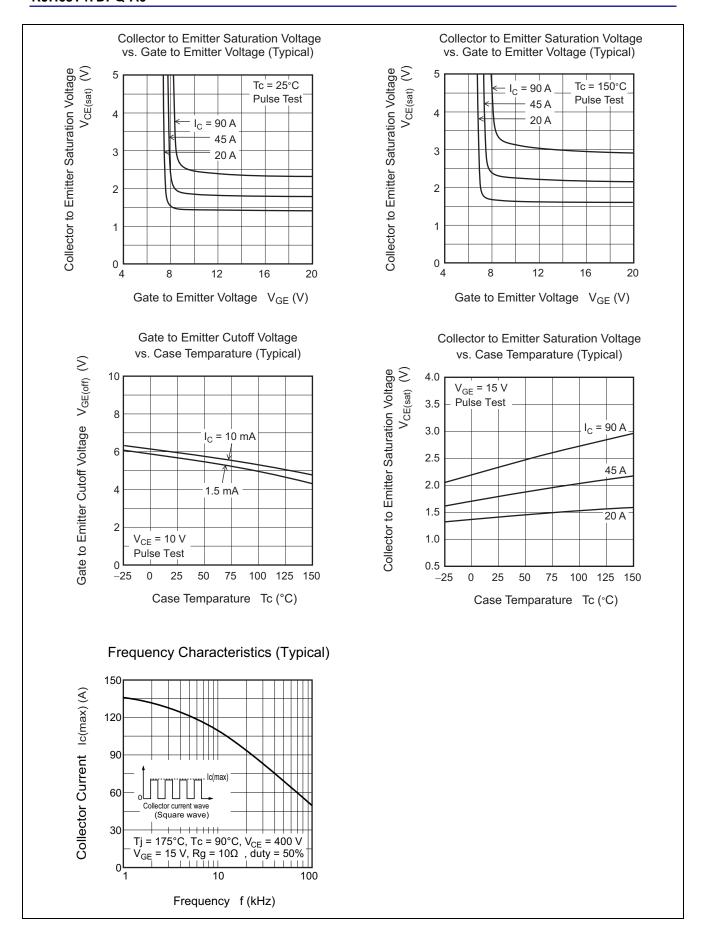
FRD forward voltage	VF	_	1.7	2.2	V	I _F = 15 A ^{Note3}
FRD reverse recovery time	t _{rr}	_	100	_	ns	I _F = 15 A, di _F /dt = 300 A/μs

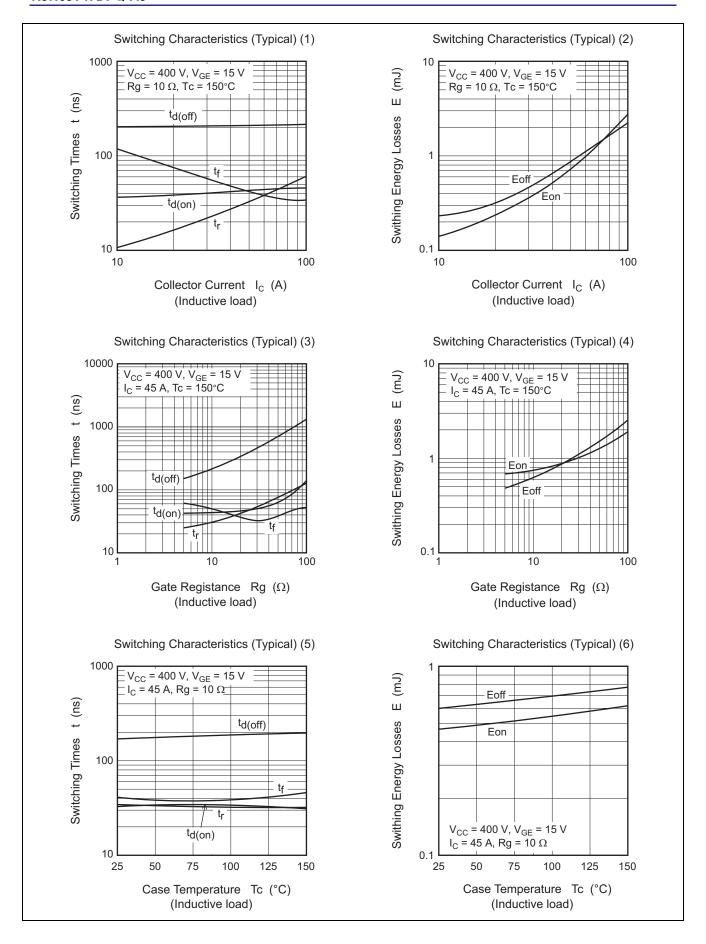
Notes: 3. Pulse test

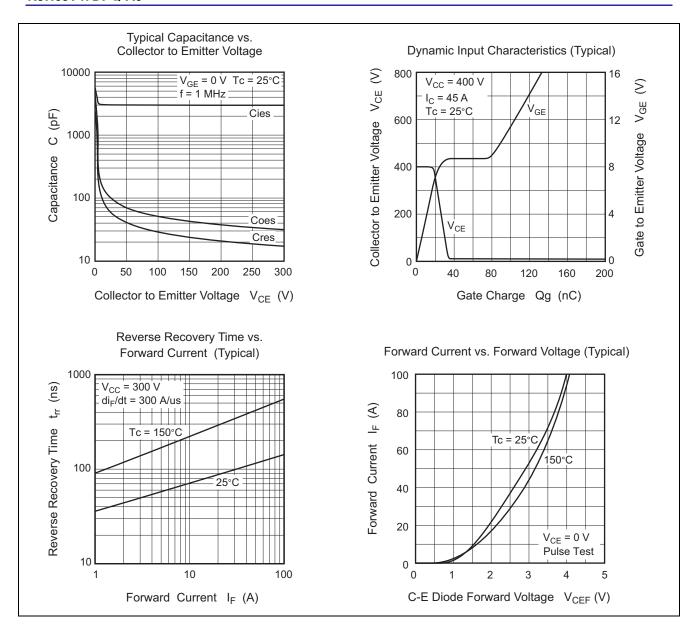
4. Switching time test circuit and waveform are shown below.

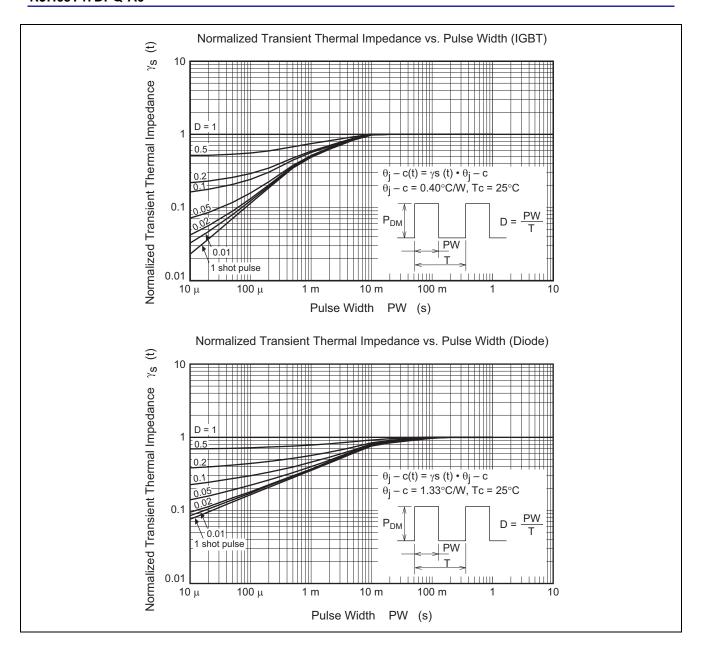
Main Characteristics

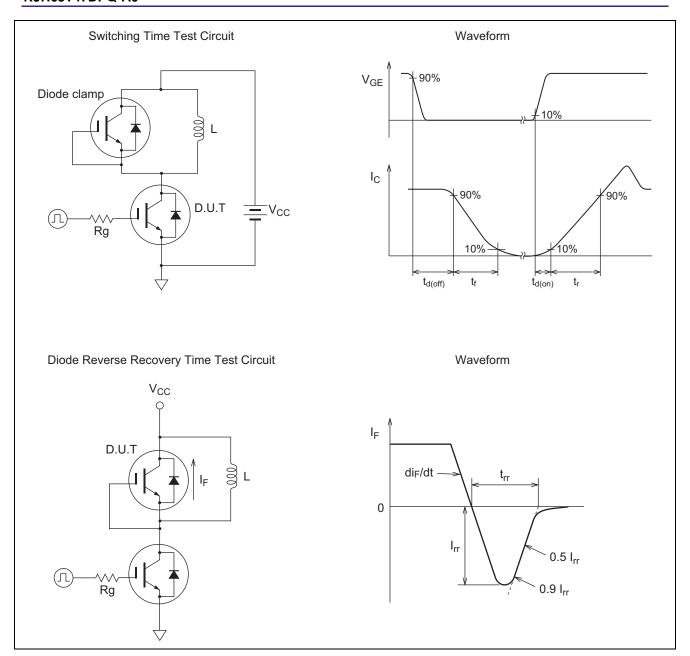




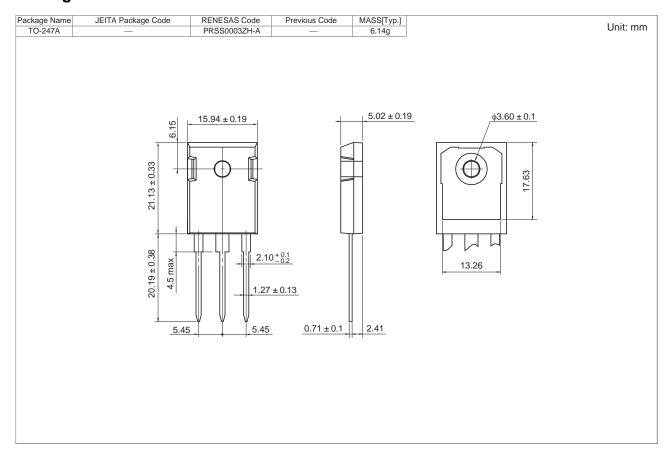








Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJH65T47DPQ-A0#T0	240 pcs	Box (Tube)

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