

PSoC 6 BLE Pioneer Kit and PSoC Creator 4.2 Beta - Limitations and Known Issues – KBA221482

Question: What are the current limitations and known issues with the CY8CKIT-062-BLE PSoC 6 BLE Pioneer Kit and PSoC Creator 4.2 Beta software?

Answer: Refer to the following documents to understand the current limitations and known issues:

- <u>PSoC 6 BLE Pioneer Kit Release Notes</u>, available in the PSoC 6 Community webpage (*cypress.com registration required*)
- PSoC Creator 4.2 Release Notes, available in the PSoC 6 Community webpage (cypress.com registration required)
- PSoC 6 MCU Datasheet and Errata, available in the PSoC 63 with BLE and PSoC 62 webpages

For a detailed list of PSoC 6 MCU documentation and design tools, visit www.cypress.com/PSoC6



Related Categories:

Keywords: [PSoC 6, errata, limitations, known issues]

Product Family: [PSoC 6 MCU]

Related Tags: [Select the Tags by clicking the checkbox associated to the Tags]

Clocks & Buffers Tags

☐ Adapter	Algorithm	Bitmap	☐ Buffer	☐ Bypass	☐ CLKMAKER
☐ CML	☐ CY3670	☐ CY3672	☐ CY3675	☐ CY36800	☐ Cascade
☐ Charge Pump	☐ Clock Tree	Clocks	Clocks and Buffers	☐ ComLink	☐ Crystal Oscillators
☐ CyClock	☐ CyClockWizard	☐ CyberClocks	☐ CyberClocks Online	☐ Cycle to Cycle	DCXO
☐ Delay	☐ Deterministic Jitter	☐ Differential	Divider	☐ Duty Cycle	□ ЕМІ
☐ Enhanced Performance	☐ Ent	☐ Entrant	☐ FNOM	☐ Factory	☐ Failsafe
☐ Fanout	☐ FastEdge	☐ Feedback	Field	Flexo	Frequency
☐ Frequency Margining	☐ Function Select	Generators	☐ Ground Bounce	☐ HPB	☐HSTL
☐ Hershey Kiss	☐ I2C	☐ IBIS Model	☐ Impedance	☐ Input	☐ InstaClock
☐ Inverted	☐ Jed	☐ Jedec	Jitter	LVCMOS	LVDS
LVPECL	☐ Layout	Lexmark Profile	☐ Loop Bandwidth	MoBL	☐ Modulation Rate
Multiplier	□NZDB	☐ Non-Volatile Memory	☐ OTP	Offset	Oscillator
Output	☐ Overvoltage	☐ PCI Express	☐ PLL	☐ PPM	☐ PREMIS
Period	☐ Phase	☐ Phase Noise	☐ Power Supply Noise	☐ Program	☐ Programmable
☐ Propagation	☐ Pullability	RMS	Rambus	Random Jitter	Reference
Rise Fall Time	RoboClock	☐ Schematic	☐ Serial	☐ Signal Integrity	Skew
Socket	☐ Specialty Clocks	☐ Spread %	☐ Spread Aware	☐ Spread Profile	☐ Spread Spectrum
Synthesizer	TCXO	☐ TIE	☐ Termination	☐ Thermal	Translator
Undervoltage	UniClock	Unit Interval	□ vco	□ vcxo	☐ Volatile Memory
☐ Voltage Droop	□xcg	□XDR	□xo	☐ Xtal	ZDB
Zepto					



Lighting & Power Control Tags

☐ Board Layout ☐ Bo	oost Regulato	r 📗 B	uck Regulator	☐ CY3	261	☐ CY326	67 CY3	3268		
☐ CY3269 ☐ C	olor Mixing	C	urrent Sense	☐ DAL	.I	☐ DMX	☐ FN	Pins		
☐ HB LEDs ☐ H	ysteretic Cont	roller	OSFETs		PT	☐ Modul	ators	SM		
☐ Programming ☐ S	SDM	□ S	chematics	Swit	ching Regulators	☐ Trip	☐ Volt	age Regulator		
							·			
Wireless/RF Tags										
☐ 2.4 GHz	□ 8□)R	☐ Antenna		☐ Audio		☐ AgileHID)	☐ Bridg	 ge
☐ CY3271	□ C)	Y3630	☐ CY4636		☐ CY4672		CYFI		CYF	ISNP
☐ CYRF6936	□ C	YRF7936	☐ Channel		DDR		DSSS		GFS	K
HID	□н	JB	□IRQ		☐ Interference A	voidance	☐ Keyboard		Link	Budget
Mouse	□No	ode	☐ PMU		☐ PN Code		☐ PRoC		☐ PRo	C-CS
☐ PRoC-EMB	☐ PF	RoC-TT	☐ PRoC-US	В	☐ PRoC-UI		☐ Power Am	plifier	☐ Prea	amble
☐ Pseudo Noise code	RF	=	RSSI		Range		Remote		SCD)
SDR	□ SC	OP .	SPIM		☐ Star-Network	Protocol	Streaming		☐ Trac	kpad
Wireless	□W	ireless USB	☐ Wireless U	JSB LP	☐ Wireless USB	LS	☐ Wireless U	JSB LR	☐ Wire	eless USB NL
☐ Wireless Capacitive	Touch 🗌									
Memory Tags										
Address Bus	Async	☐ Aı	utomotive		Autostore		☐ BHE / BLE	BUSY		

Address Bus	☐ Async	☐ Automotive	☐ Autostore	☐ BHE / BLE	BUSY
☐ Battery Backup	☐ Burst	☐ Bus Contention	Clock	☐ DDR	☐ Data Bus
☐ Data Integrity	☐ Data Retention	Datasheet	☐ Dual Port	☐ Echo Clocks	FIFO
☐ FPGA	☐ Flags	☐ Flowthrough	Fullflex	☐ FRAM	HOLD
☐ HD-FIFO	□HSB	□ I2C	□INT	☐ Impedance Matching	☐ Interface
☐ Interleaving	☐JTAG	☐ Junction Temperature	☐ Low Power	☐ MSL	☐ Migration Path
☐ MoBL	Models	□ NoBL	ODT	Obsolete	☐ PCB
☐ PLL / DLL	☐ Packaging	☐ Parallel	☐ Parity	☐ Part Decoder	☐ Part Difference

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☐ Password Protection	☐ Pin Configuration	☐ Pipeline	☐ Power Consumption	on Power On State	Processor
□QDR	☐ Qualification Reports	☐ Quality	RECALL	RTC	Race Condition
Read	SER	SPCM	SPI	SRAM	Serial
☐ Signal Integrity	Sync	□ Technology	☐ Termination	☐ Timing ☐] Vcap
□ Vddq	☐ Voltage Levels	☐ Vref	☐ Width / Depth Expansion	Write	nvSRAM
uPower]
Interface Tags					
☐ 10B/8B Decoder	3 Level Inputs	ALDEC	☐ ATM	Altera	BIST
BSDL	Biasing	CDR	CML	CPLD	☐ CY3900i
☐ CY3950i	CYUSBISRPC	☐ Cable	☐ Cable Driver	☐ Channel Bonding	☐ Clock Multiplier
☐ Clocking Modes	☐ Coaxial Cable	☐ Copper Cable	☐ Coupling	☐ Crystal	☐ Current
☐ DVB	☐ Data Characte	r Data rate	☐ Delta39K	☐ Deserialize	Deserializer
☐ Dual Channel	☐ EPLD	☐ EPROM	☐ ESCON	☐ Elasticity Buffer	☐ Equalizer
Error	☐ Evaluation Boa	ard FPGA	☐ Fiber Optics	☐ Flash370i	Framer
☐ Framing Character	☐ Framing Mode	☐ Gigabit Ethernet	Hex	☐ High Speed Serial Link	ks Hotlink
Hotlinkl	HotlinkII	□IP	Isr	☐ Jtag	LFI
LVTTL	☐ Logic Level	☐ Loop Back	☐ Macrocell	☐ Max340	☐ Model
☐ Non-Volatile	OC-1	☐ OC-2	☐ OC-3	☐ Operating System	☐ PAL
☐ PECL	☐ PLD	☐ PLL	☐ Parallel Input	☐ Parallel Output	☐ Parity
☐ Phase Align Buffer	☐ PoF	☐ Point To Multi Point	☐ Point To Point	☐ Power Supply	☐ Programming
Quad Channel	☐ Quantum38K	Receiver	☐ Recovered Clock	☐ Redundant Outputs	☐ Reference Clock
☐ Report File	Reprogramma	ble SMA	SMPTE	☐ SMPTE-259M	☐ SMPTE-292M
SONET	SPICE	SPLD	STAPL	SVF	☐ Serial Input
☐ Serial Output	Serializer	Simulator	Size	☐ Special Character	☐ Specification
TTL	☐ Temperature	☐ Third Party Tool	Transceiver	Transmitter	USBISRPC Cable
Ultra37K	☐ User Guide	☐ VHDL	☐ Verilog	☐ Voltage	□Warp
☐ Word Sync Sequence					



PSoC 1 Tags

☐ ADC	☐ ADCINC	☐ Analog	☐ Analog Bus	☐ API	Assembly Language
☐ Boot.asm	☐ Bootloader	☐ BSDL Files	☐ Build Errors	☐ Calibration	☐ Capsense
☐ Clock	☐ Clock Synchronization	☐ Cloning	☐ Column Clock	☐ Communication	☐ Comparator
☐ Compiler	☐ Counter	☐ CPU Speed	☐ Crystal	☐ CT Block	☐ DAC
☐ Debugging	☐ Decimator	☐ Delta Sigma ADC	☐ Development Kit	☐ Digital	☐ Dynamic Reconfiguration
☐ ECO	☐ EEPROM	☐ Errata	Filter	☐ Flash	☐ Flash Security
☐ Global Resources	☐ GPIO	☐ Hex File	☐ I2C	☐ I2C-USB Bridge	☐ ICE Cube
☐ Installation	☐ Internet Explorer	☐ Interrupt	☐ ISR	☐ ISSP	☐ Large Memory Model
LCD	License	☐ MAC	☐ MiniProg1	☐ MiniProg3	☐ Mux
☐ Mux Bus	OCD	☐ Offset	Pod	☐ Port and Pins	☐ Power Management
☐ Production Programmer	☐ PWM	☐ RAM	RTC	□SAR	☐ SC Block
SMP	☐ SPI	System Level Design	☐ Timer	☐ UART	□USB
☐ USBUART	☐ Watchdog	☐ Amplifier UM	☐ Analog Reference	☐Analog UM	☐ Build Tools
☐ Communication UM	☐ CRC UM	CYFI	☐ Device Programming	☐ Digital UM	☐ DTMF
☐ Fan Controller UM	☐ Firmware UM	☐ FMEA	☐ Port Expander	☐ PSoC Power System Architecture	□Voltage Sequencer UM
PSoC 3/4/5 Tags		,	,	,	
Component Tags					
☐ Analog Hardware Mux	☐ Analog Mux	☐ Boost Converter	☐ Bootloader / Bootloadable	☐ CAN	☐ CapSense_CSD
☐ Character LCD	☐ Clock	☐ Comparator	Control / Status Register	☐ Counter	☐ CRC
☐ DAC	☐ Debouncer	☐ Delta Sigma ADC	☐ DFB	☐ DFB Assembler	☐ Die Temperature
☐ Digital Comparator	☐ Digital Multiplexer	☐ DMA	☐ EEPROM	emFile SPI Mode	☐ EMIF
☐ Ezl2C Slave	☐ Fan Controller	☐ Filter	☐ Frequency Divider	☐ Glitch Filter	☐ Global Signal Reference
☐ Graphic LCD	☐ I2C /I 2S	□iAP	☐ Interrupt	LIN	☐ Logic Gates



☐ Lookup Table	☐ Manual Routing	☐ MDIO	Mixer	☐ Opamp	□PGA
☐ Ports and Pins	☐ Power Monitor	□PRS	PWM	Quadrature Decoder	Resistive Touch
RTC	☐ RTD Calculator	Sample / Track and Hold	☐ SAR ADC	☐ SAR Sequencer	☐ Segment LCD
SGPIO	☐ Shift Register	☐ Sleep Timer	SM / PMBus	SPDIF	SPI
Sync	☐ Thermistor Calculator	☐ Thermocouple Calculator	□TIA	☐ Timer	☐ TMP05 Interface
☐ TrimMargin	□UART	□ UDBClkEn	USBFS	USBMIDI	USBUART (CDC Interface)
☐ Voltage Fault Detector	☐ Voltage Sequencer	☐ Vref	☐ WaveDAC		
General Tags					
☐ Analog Bus	☐ Analog Global Bus	☐ Analog Mux Bus	☐ API	☐ Application Specific	Assembly Language
☐ Bootloader Host	☐ Boundary Scan / BSDL	☐ Bridge Control Panel	☐ Build Settings	☐ Clock	☐ Compiler - GCC
☐ Compiler - KEIL	☐ Compiler - MDK	☐ Compiler - RVDS	☐ Cortex-M0	☐ Cortex-M3	☐ Creator Registration
Custom Component Interconnect	☐ Datapath Configuration Tool	☐ Debugging	☐ DMA Wizard	□ DVK	□ ECO
☐ Errata	☐ Error Message	☐ Flash	☐ Hex File	☐ Installation	☐ ISSP / HSSP
☐ KEIL Registration	Linux Platform	☐ Low Power Modes	☐ LVD / HVD	☐ MFi	☐ MiniProg3
Optimization	☐ Programmer COM	☐ PSoC Creator	☐ PSoC Programmer	Reset	□RTOS
Schematic	Silicon	☐ Software Download	☐ STA	☐ Supply Voltage	System Reference Guide
☐ Verilog	☐ Watchdog	☐ Windows Platform			
Kit Tags					
☐ CAN / LIN EBK	☐ CapSense Expansion EBK	CY8CKIT-001 Kit	☐ CY8CKIT-030 / 050 Kit	CY8CKIT-042 Kit	☐ Digital Audio EBK
☐ First Touch Kit	LCD Segment Drive EBK	☐ MFI EBK	Power Supervisor EBK	PSoC 3/4/5 Processor Module	☐ Thermal Management EBK
Touch Sensing Tags					
ADC	☐ Air gap	Backlighting	☐ Bleeder Resistor	Bootloader	☐ CMOD
☐ CSA	CSD	☐ CSD Parameters	☐ CSD2X	☐ CSDADC	☐ CSDAUTO
☐ CY3203A	☐ CY3213A	☐ CY3214	☐ CY3218	☐ CY3280-20x34	☐ CY3280-20xx6



☐ CY3280-21x34	☐ CY8C20x34	☐ CY8C20xx6	☐ CY8C21x34	CY8C21xxx-CapSense Express	☐ CY8C24x94
☐ CY8CMBR2044	☐ CapSense Express	☐ Circuit Housing	☐ Conductive Objects	Configuration	Diplexing
Dynamic Reconfiguration	☐ EEPROM	□ ЕМІ	□ESD	☐ Errors	☐ FR4
Filters	☐ Finger Threshold	Flex PCB	☐ I2C	☐ I2C-USB Bridge	□IDAC
☐ IMO and Prescaler	□ ІТО	☐ Layout Guidelines	☐ Metal	□ Noise	Overlay
☐ PSoC3 CapSense	☐ Parasitic Capacitance	☐ Pathfinder	☐ Power Consumption	☐ Proximity	SNR
SPI	☐ Scanning Techniques	Schematic	Sensors	Shield	Sliders
☐ SmartSense	Tuning	UART	□ Water	☐ Water Proofing	
USB Controllers Tags					
8051	☐ AN2131	AT2LP	☐ ATA / ATAPI	ATA Commands	Asynchronous
Auto Mode	Bandwidth	Blaster	☐ Bulk Transfer	☐ Bus Power	☐ C#
☐ C++	CAT5	☐ CF Card	☐ CY3216	☐ CY3649	☐ CY3654
☐ CY3655	CY3660	☐ CY3662	☐ CY3664	☐ CY3674	☐ CY3681
☐ CY3684	☐ CY3685	☐ CY3686	☐ CY4605	☐ CY4606	☐ CY4611B
☐ CY4615	CYUSB	☐ Clock	☐ Compliance	☐ Control Center	Control Transfer
☐ Crystal	☐ CyConsole	☐ DLL	☐ Debug	☐ Descriptors	☐ Driver
☐ EEPROM	☐ EZ-HOST	☐ EZ-OTG	☐ EZ-USB	☐ Emulation	☐ EnCoreII
☐ EnCoreIII	☐ EnCoreV	☐ Encore	☐ Endpoint	☐ Enumeration	☐ Errata
FIFO	□FX	☐ FX1	☐ FX2	☐ FX2LP	Firmware
☐ Firmware Debug	☐ Flags	Framework	☐ Full Speed	GPIF	HDD
HID	☐ HX2	☐ HX2LP	☐ Hi-Lo Programmer	☐ High Speed	☐ Host Application
Hub	☐ I2C	☐ ICE	☐ IN Transfer	☐ Interrupt Transfer	☐ Interrupts
☐ Isochronous Transfer	☐ Keil	☐ Keyboard	Layout	Library	Loader
☐ Low Speed	☐ M8A	☐ M8B	☐ Manual Mode	☐ Mass Storage	☐ Memory
☐ Mouse	□ NX2LP	☐ NX2LP-Flex	☐ Nand Flash	☐ Nand Manufacturing Utility	□ОТР



OUT Transfer	☐ Port IO	Register	Renumeration	Report	Reset
☐ SFR	SIE	☐ SL811HS	SPI	☐ SX2	☐ Schematic
☐ Schematic Review	☐ Screamer	☐ Script	☐ Self Power	☐ Slave FIFO	☐ Streaming
SuiteUSB	Synchronous	☐ TX2	☐ TX2UL	☐ Tetra Hub	☐ Throughput
Timer	□UART	UDMA	USBFS	☐USBUART	USBSerial
☐ Vendor Command	☐ Video Class	WHQL	□WLK	☐ cyapi	uVision
SuperSpeed					
☐ FX3	☐ ADMux	☐ ARM926EJ -S	Bootloader	□ DMA	☐ Eclipse
☐ FX3 GPIO	☐ FX3 Power Management	☐ FX3 Power supply	☐ FX3 SDK	☐ GPIF II	☐ HS-OTG
☐JTAG	☐ LPP	□MSC	☐ Oscillator	RTOS	☐ SD Card
Slavefifo	☐ USB 3.0	USB Compliance Test	USB Host	□UVC	
☐ FX3S	☐ eMMC	SDIO	☐ S-Ports	Bay	☐ Benicia
☐ HX3	□ ASSP	☐ Battery charging	Bootloader	☐ Cortex-M0	☐ EEPROM
GPIOs	☐ HX3 Power Management	☐ HX3 Power supply	☐ I2C	☐ In-system programming	☐ Oscillator
USB 3.0 hub	USB Shared Link™				

Automotive MCU Tags

Traveo	☐ S6J3110	☐ S6J3120	☐ S6J3200	☐ S6J3300	☐ S6J3310
☐ S6J3320	☐ S6J3330	☐ S6J3340	☐ S6J3350	☐ S6J3400	☐ S6J3500
☐ S6J326C	☐ S6J324C	☐ S6J327C	☐ S6J328C	☐ S6J32DA	☐ S6J32BA
☐ S6J3360	☐ S6J3680	☐ S6J32G0			
☐ FR81S	☐ MB91520	☐ MB91550	☐ MB91580	☐ MB91570	☐ MB91590
☐ FCR4	☐ MB9DF120	☐ MB9EF220	☐ MB9D560	☐ F2MC-16FX	☐ MB96300
☐ MB96600	☐ MB96800	☐ CAN	☐ CAN FD	LIN	☐ Flexray
☐ Ethernet	AUTOSAR	☐ Serial	Timer	☐ Watchdog	☐ A/D Converter
SHE	Security	☐ Current	☐ Interrupt	Graphics	□ Document
☐ CPU	☐ Clock	☐ Mode	Reset	☐ Real Time Clock	BootROM
☐ Flash Memory	RAM	☐ Backup RAM	☐ MPU	☐ PPU	☐ DMA



☐ Multi-Function Serial	UART	☐ CSIO	☐ I2C	☐ Base Timer	QPRC
☐ CRC	☐ Port	☐ External Bus	☐ DDR HSSPI	☐ Hyper Bus	☐ Parity
☐ PSS	☐ Low Power	SMC	LCDC	☐ Media LB	☐ DAC
☐ Audio DAC	☐ I2S	☐ PCMPWM	Sound	☐ APIX	☐ Motor
☐ Wave Generator	RDC	□MVM	☐ Flash Programmer	☐ Tool	



Knowledge Base Article Type: [Select the category by clicking the checkbox associated]

KB Type-1	KB Type-2		
	☐ ByteCraft		
☐ Compiler	☐ Imagecraft		
Compiler	☐ Keil		
	HiTech		
	☐ Training/Things you should know		
	Component Architecture		
	☐ Component/Project management		
Composert Development	☐ Datapath		
Component Development	☐ Analog components		
	☐ Digital components		
	☐ Component software/tools		
	☐ Component Testing		
☐ Development Kits	☐ FirstTouch Kit		
☐ Development Tools			
Device Drivers	☐ Pullability		
Device Drivers	☐ Mass Storage		
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Firmware			
General			
	☐ Digital		
☐ Hardware	Specifications		
	☐ Analog		
☐ Known Problems and Solutions			
	□ 8051		
☐ Microcontrollers	☐ M8		
	☐ M8C		
	☐ Traveo		



	☐ FCR4
	□FR
	☐ F2MC
Modules	
☐ Platforms	☐ MacOS X
	☐ Windows
Protocols	HID
Protocois	☐ I2C
☐ Quality	
☐ Reference Designs	
⊠ Software	☐ PSoC Designer
△ Software	□ PSoC Creator □
□ Haar Madulaa	Analog
User Modules	☐ Digital



Document History

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Rev.	ECN No.	Orig. of Change	Description of Change
**	5898229	IYM	New KBA